## Regent Education and Research Foundation Department of EE Routine for 2<sup>nd</sup> Internal Test (B.TECH)

B.Tech 2 <sup>nd</sup> Year(EE)			
m·		Date: 16/11/2017	11 20 12 00
Time:	10:30 – 11:00	11:00-11:30	11:30 – 12:00
Subject:	M (CS) 301	EC(EE)301	EC(EE)302
	Numerical	Analog Electronic	Digital Electronic
	Methods	circuits	circuit
Date: 17/11/2017			
Time:	10:30 - 11:00	11:00- 11:30	11:30 – 12:00
Subject:	M302	EE-301	EE-302
	<b>Mathematics-III</b>	Electric Circuit theory	Field theory
B.Tech 3 <sup>rd</sup> Year(EE)			
Date: 16/11/2017			
Time:	1:00-1:30	1:30-2:00	2:00 - 2:30
Subject:	EE-501	EE-502	HU-501
	Electric machine-	Power system-I	<b>Economics for</b>
	II	·	Engineers
Date: 17/11/2017			
Time:	1:00-1:30	1:30-2:00	2:00 - 2:30
Subject:	EE-503	EE-504 C.	
	Control system-I	Micro Processor &	
	·	Micro controller	
B.Tech 4 <sup>th</sup> Year(EE)			
Date: 16/11/2017			
Time:	3:00-3:30	3:30-4:00	4:00-4:30
Subject:	EE-702	EE-701	EE-703 A.
•	<b>Utilization of</b>	Electric drive	Power system-III
	Electric power		
Date: 17/11/2017			
Time:	3:00-3:30	3:30-4:00	4:00-4:30
Subject:	EE-704 D.	EE-705 A.	
•	Renewable & Non	Computer Network	
	conventional	•	
	Energy		
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